*FIG. 2*

```

    graph TD
        ROM[ROM] --- μP[μP]
        μP --- RAM[RAM]
        μP --- MC[MOTOR CONTROL]
        MC --- PDET[POSITION DETECTOR]
        MC --- RWC[READ/WRITE CONTROL]
        MC --- ACTC[ACT CONTROL]
        PDET --- 44
        RWC --- 40
    
```

2/4

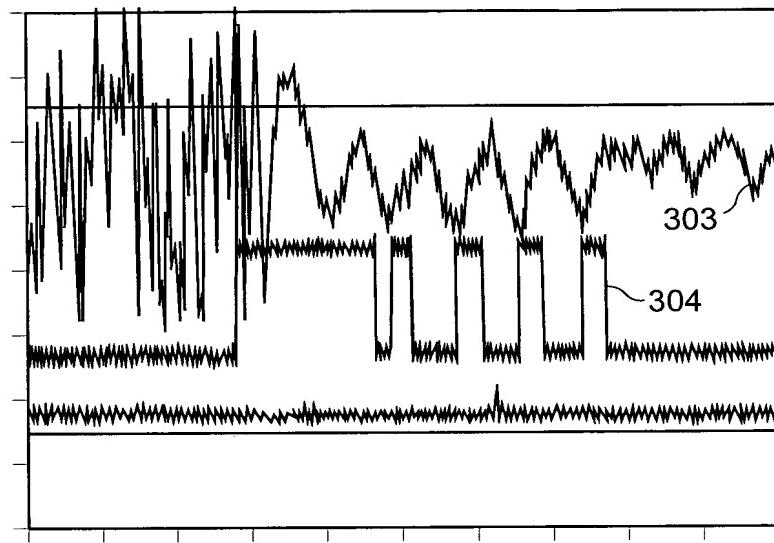


FIG. 3

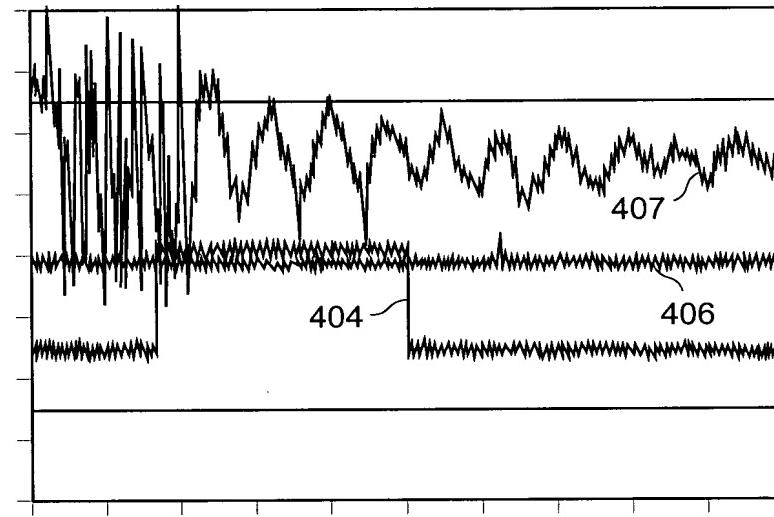


FIG. 4

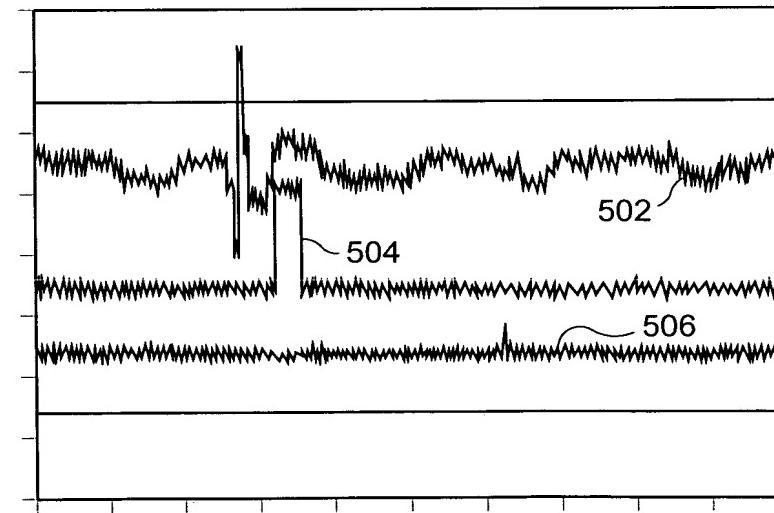


FIG. 5

FIG. 7-1

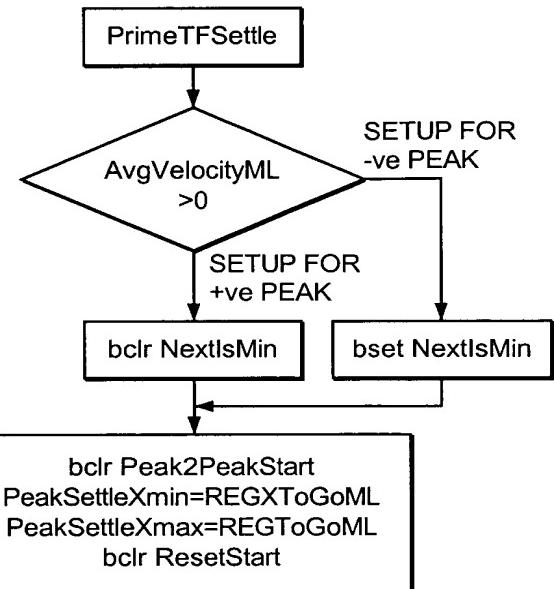


FIG. 6

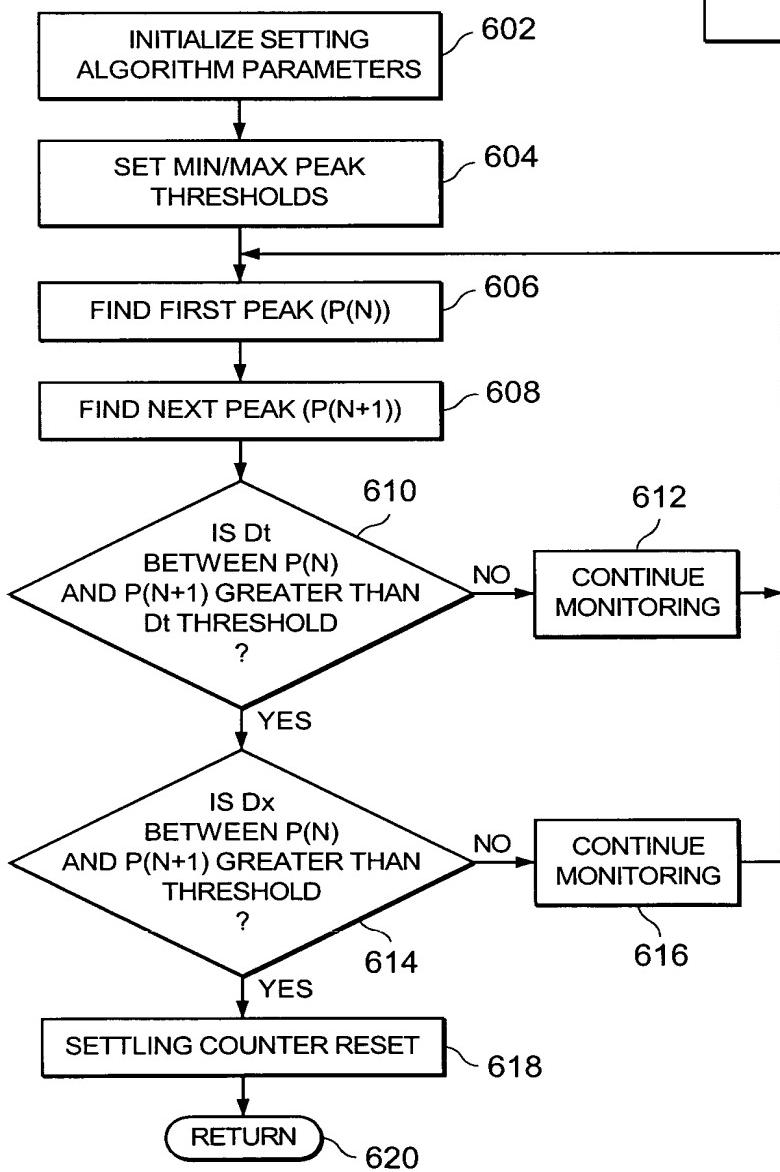


FIG. 7-2

